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(19) **United States**(12) **Patent Application Publication****Bobde et al.**(10) **Pub. No.: US 2015/0021682 A1**(43) **Pub. Date: Jan. 22, 2015**(54) **NORMALLY ON HIGH VOLTAGE SWITCH**(71) Applicant: **Alpha and Omega Semiconductor Incorporated**, Sunnyvale, CA (US)(72) Inventors: **Madhur Bobde**, Santa Clara, CA (US);
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Karthik Padmanabhan, San Jose, CA (US)(73) Assignee: **Alpha and Omega Semiconductor Incorporated**, Sunnyvale, CA (US)(21) Appl. No.: **13/945,784**(22) Filed: **Jul. 18, 2013****Publication Classification**(51) **Int. Cl.**
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H01L 29/78 (2006.01)(52) **U.S. Cl.**CPC **H01L 29/66666** (2013.01); **H01L 29/7827** (2013.01)USPC **257/330**; **438/270**(57) **ABSTRACT**

In some embodiments, a normally on high voltage switch device ("normally on switch device") incorporates a trench gate terminal and buried doped gate region. In other embodiments, a surface gate controlled normally on high voltage switch device is formed with trench structures and incorporates a surface channel controlled by a surface gate electrode. The surface gate controlled normally on switch device may further incorporate a trench gate electrode and a buried doped gate region to deplete the conducting channel to aid in the turning off of the normally on switch device. The normally on switch devices thus constructed can be readily integrated with MOSFET devices and formed using existing high voltage MOSFET fabrication technologies.

